



Advanced N-Ch Power MOSFET

SWITCHING REGULATOR APPLICATIONS

PIN Connection

Features

• High Voltage : BV_{DSS}=650V(Min.)

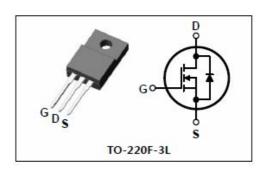
• Low Crss: Crss=18pF(Typ.)

• Low gate charge : Qg=35nC(Typ.) • Low RDS(on) : RDS(on)=0.75 Ω (Max.)

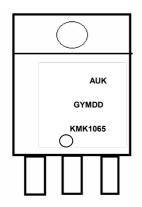
Ordering Information

Type NO.	Marking	Package Code

KMK1065F KMK1065• TO-220F-3L



Marking Diagram



· Da Lian

Column 1 : Manufacturer

Column 2: Production Information

e.g.) GYMDD

-. G: Factory management code

-. YMDD: Date Code (year, month, date)

Column 3: Device Code

Absolute maximum ratings (Tc=25 C unless otherwise noted)

Characteristic	Symbol		Rating	Unit		
Drain-source voltage	V _{DSS}		650	V		
Gate-source voltage		V _{GSS}	±30	V		
Drain current (DC) *	Tc=25℃		10	Α		
	10	Tc=100°C	5.8	Α		
Drain current (Pulsed)*	Ідм		38	Α		
Power dissipation	PD		40	W		
Avalanche current (Single) ②	Ias		10	Α		
Single pulsed avalanche energy ②	Eas		Eas		480	mJ
Avalanche current (Repetitive) ①	Iar		Iar		10	A
Repetitive avalanche energy ①	Ear		11.6	mJ		
Junction temperature		T ı	150	°C		
Storage temperature range	Tstg		-55~150			

^{*} Limited by maximum junction temperature

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Characteristic		Symbol	Тур.	Max	Unit
Thermal	Junction-case	Rth(J-C)	-	3.1	°C/W
resistance	Junction-ambient	Rth(J-A)	-	62.5	°C/W

Electrical Characteristics (Tc=25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Drain-source breakdown voltage	BV _{DSS}	ID=250uA, VGS=0V	650	-	-	V
Gate threshold voltage	V _{GS(th)}	ID=250uA, VDS=VGS	2.0	-	4.0	V
Drain-source cut-off current	IDSS	V _{DS} =600V, V _{GS} =0V	-	-	1	uA
Gate leakage current	Igss	$V_{DS}=0V$, $V_{GS}=\pm30V$	-	-	±100	nA
Drain-source on-resistance 4	RDS(on)	V _G s=10V, I _D =5.0A	-	0.60	0.75	Ω
Forward transfer conductance ④	G fs	V _{DS} =10V, I _D =5.0A	-	8.0	-	S
Input capacitance	Ciss		-	2000	2350	
Output capacitance	Coss	V _{GS} =0V, V _{DS} =25V f=1 MHz	-	160	215	pF
Reverse transfer capacitance	Crss		-	18	-	
Turn-on delay time	td(on)		-	23	-	
Rise time	tr	V _{DD} =300V, I _D =10A	-	69	-	
Turn-off delay time	td(off)	$R_G=25\Omega$ 34	-	144	-	nc
Fall time	tf		-	77	-	ns
Total gate charge	Qg	Vac-490V Vcc-10V	-	35	57	
Gate-source charge	Qgs	VDS=480V, VGS=10V ID=10A 34	-	9	-	nC
Gate-drain charge	Qgd		-	10	-	

Source-Drain Diode Ratings and Characteristics (Tc=25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Source current (DC)	Is	Integral reverse diode	-	-	10	۸
Source current (Pulsed) ①	Isм	in the MOSFET	-	-	40	A
Forward voltage 4	VsD	V _G s=0V, I _S =10A	-	-	1.4	V
Reverse recovery time	trr	Is=10A, V _G s=0V	-	470	-	ns
Reverse recovery charge	Qrr	dI _F /dt=100A/us	-	6	-	uC

Note;

① Repetitive rating: Pulse width limited by maximum junction temperature

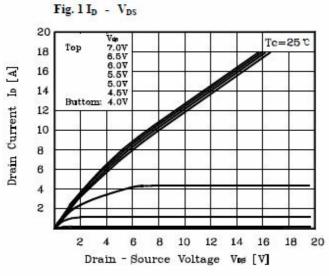
② L=10mH, Ias=9.5A, Vdd=50V, RG=25 Ω , Starting Tj=25 $^{\circ}$ C

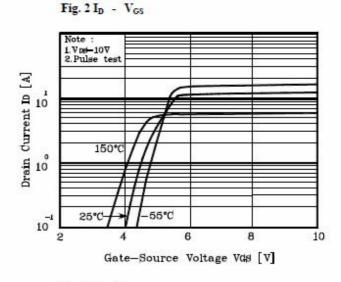
③ Pulse Test: Pulse width≤300us, Duty cycle≤2%

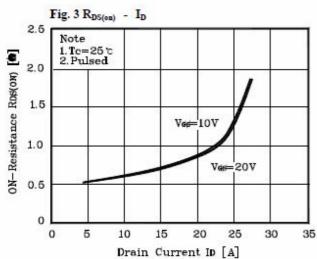
4 Essentially independent of operating temperature

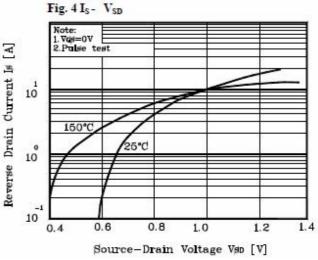
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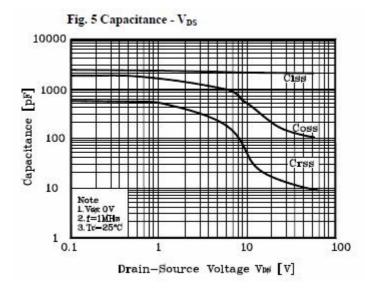
Electrical Characteristic Curves

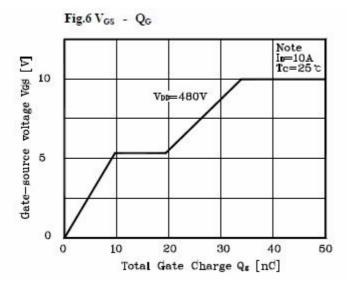




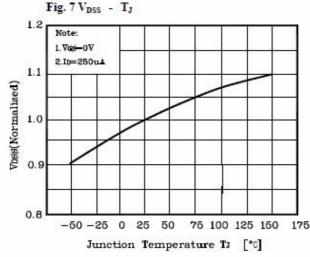


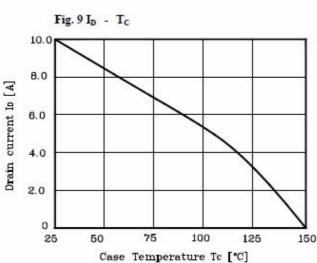


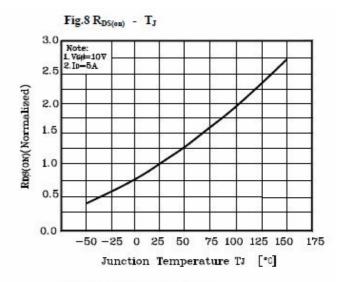




Electrical Characteristic Curves







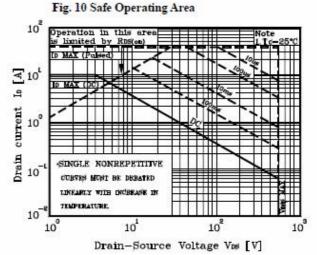


Fig. 11 Gate Charge Test Circuit & Waveform

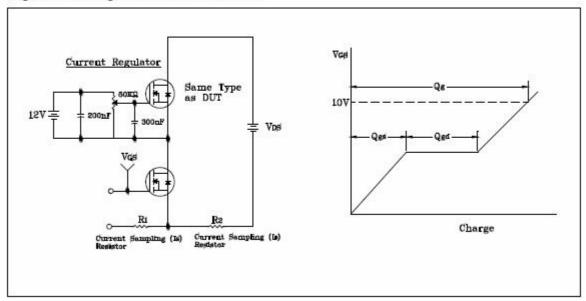


Fig. 12 Resistive Switching Test Circuit & Waveform

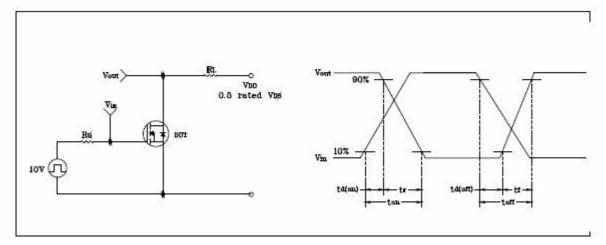


Fig. 13 E_{AS} Test Circuit & Waveform

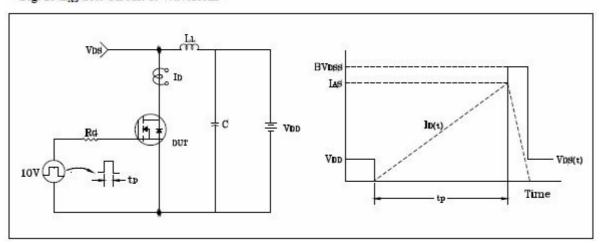
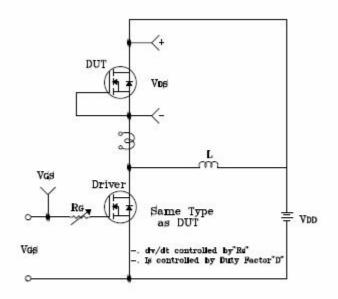
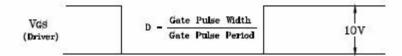
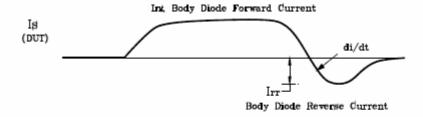
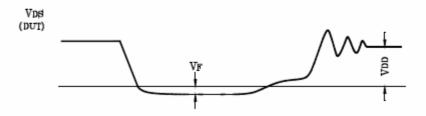


Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform



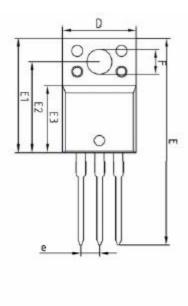




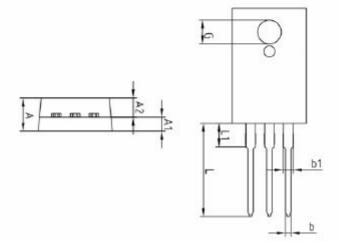


KMK1065FPI

Outline Dimension unit: mm







		MILLIMETER	NOTE		
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	NOTE	
Α	-	-	4.60		
A1	2.45	2.50	2.55		
A2	1.95	2.00	2.05		
ь	0.65	0.75	0.85		
ь1	1.07	1.27	1.47		
С	0.40	0.50	0.60		
C1	2.70	2.80	2.90		
D	9.90	10.00	10.10		
E	28.00	-	28.60		
E1	15.50	15.60	15.70		
E2	12.30	12.40	12.50		
E3	9.15	9.20	9.25		
F	3.30	3.40	3.50		
G	3.10	3.20 2.54 BS	3.30		
е					
L	12.40	_	13.00		
L1					

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